

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	2	"5911059".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 20:52
S2	0	(703/28.ccls. 714/28.ccls.) and microcontroller and (shar\$4 adj2 clock\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 17:26
S3	1	(703/28.ccls. 714/28.ccls.) and (shar\$4 adj2 clock\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 17:27
S4	6	(703/28.ccls. 714/28.ccls.) and (shar\$4 with clock\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 17:29
S5	2	"5802290".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 17:31
S6	0	09/975105	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 17:31
S7	1	(broda.xp.) and cypress.as.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 20:52
S8	1	(broda.xa.) and cypress.as.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 20:53
S9	106	cypress.as. and fpga	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 21:17

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S10	2	cypress.as. and fpga and (virtual adj microcontroller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 20:53
S11	8	("20030056071"   "5546562"   "5587957"   "5590354"   "6161199"   "6385742"   "6564179"   "6718294").PN. OR ("6957180").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/23 20:56
S12	10	("5331571"   "5574892"   "5630102"   "5758058"   "5895494"   "6134516"   "6202044"   "6581191"   "6618854"   "6810442").PN. OR ("6922821").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/23 21:16
S13	2	cypress.as. and fpga and emulator	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/23 21:17
S14	150	cypress.as. and microcontroller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 21:17
S15	23	S9 and S14	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 21:20
S16	100	(fpga and microcontroller and emulator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 21:20
S17	39	S16 and @pd<="20020101"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 21:20
S18	100	(fpga and microcontroller and emulator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/24 08:54
S19	39	S18 and @pd<="20020101"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/24 08:54

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S20	61	S18 not S19	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/24 09:10
S21	417	(lock\$step synchron\$7) and (microcontroller) and (emulator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/24 09:11
S22	40	S21 and (703/28.ccls. 714/28.ccls. 714/29.ccls. 714/30.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/24 09:11